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By: John P. ~

Date: July 17, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Anke Krasemann et al.
Applic. No. : 09/729,058
Filed : December 4, 2000
Title : Method of Producing an Oxidation-Protected Electrode for a Capacitive Electrode Structure
Examiner : Pho M. Luu - Art Unit: 2824

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INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. 1.97(C)(2)

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

07/25/2002 5762DIEI 00000097 09729058
01 FC:126 150.00 DP

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 5,939,131 (Kim et al.), dated August 17, 1999;

Japanese Patent Abstract JP 60-173867 (A) (Shinichi), dated September 7, 1985;

Japanese Patent Abstract JP 61-133657 (A) (Hitoshi), dated June 20, 1986;

K.S. Tang et al.: "Trends In DRAM Dielectrics", *Circuits & Devices*, May 1997, pp. 27-34.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications,

patents and/or publications, it is because no existing translation is readily available to the applicant.

In accordance with 37 C.F.R. 1.97 (c) (2), consideration of this Information Disclosure Statement is requested.

Enclosed is the fee in the amount of \$180.00.

Respectfully submitted,



For Applicant

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Date: July 17, 2002

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